

FORM PTO-1449 U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE JAN 20 2004 INFORMATION DISCLOSURE STATEMENT BY APPLICANT (USE SEVERAL SHEETS IF NECESSARY)	ATTY. DOCKET NO. SLRFLR.0012P	APPLICATION NO. 10/688,005
	APPLICANT Jorge Alberto Grilo	
	FILING DATE October 16, 2003	GROUP Unknown-2817

## U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE (IF APPROPRIATE)

## FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO

EXAMINER INITIAL	OTHER DOCUMENTS (INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.)	
H-c	/	Huiling Chen, et al., "Current Mirror Circuit with Accurate Mirror Gain for Low $\beta$ Transistors", 4 pages (Unknown Date)
	/	G. Palmisano, et al., "Harmonic Distortion on Class AB CMOS Current Output Stages", <i>IEEE Transactions on Circuits and Systems-II: Analog and Digital Signal Processing</i> , Vol. 45, No. 2, February 1998, pp. 243-250

EXAMINER Henry choe	DATE CONSIDERED 4/15/05
*EXAMINER: INITIAL IF CITATION CONSIDERED, WHETHER OR NOT CITATION IS IN CONFORMANCE WITH MPEP 609; DRAW LINE THROUGH CITATION IF NOT IN CONFORMANCE AND NOT CONSIDERED, INCLUDE COPY OF THIS FORM WITH NEXT COMMUNICATION TO APPLICANT.	